AMENDMENTS TO THE CLAIMS

Please cancel Claims 6, 8, 11, 21 and 25 without prejudice as their limitations have been added to their respective independent Claims. Please amend Claims 1, 7, 10, 17, 24 and 26 as follows:

1.   (Currently Amended) A method for transmitting digital video over an analog interface comprising:

accessing digital video data having a number of bits per color per pixel;

encoding said digital video data such that analog compatibility standards are preserved and said bits per color per pixel are encoded to an amplitude level; [[and]]

transmitting said encoded digital video data over an analog interface;

separating even bits and odd bits of said number of bits per color per pixel; and

transmitting said even bits and said odd bits over a New Analog Video Interface (NAVI).

2.   (Original) The method as recited in Claim 1 further comprising:
encoding six bits per color per pixel of said digital video data.

3.   (Original) The method as recited in Claim 1 further comprising:
encoding a plurality of bits of said digital video data to one of eight available amplitude levels.

4.   (Original) The method as recited in Claim 3 further comprising:
encoding said plurality of bits of said digital video data to an amplitude level between 0.0 volts and 0.7 volts.

5.   (Original) The method as recited in Claim 1 further comprising:
encoding said digital video data such that each pixel is represented by three bits wherein each of said three bits are encoded to one of eight available levels of amplitude.

6.   (Cancelled)

7.   (Currently Amended) The method as recited in Claim [[6]] 1 further comprising:

transmitting said even bits and said odd bits on successive cycles of a symbol-rate clock.

8.   (Cancelled)

9.   (Original) The method as recited in Claim 1 further comprising:
decoding said amplitude level to a brightness level compatible with a fixed-format video display.

10.   (Currently Amended) A system for transmitting digital video data over an analog video interface comprising:
an input for receiving digital video data comprising a number of bits per color per pixel;
a digital to analog video encoder coupled to said input for encoding said digital video data such that analog compatibility standards are preserved and said bits per color per pixel are encoded to an amplitude level; and
an output coupled to said digital to analog video encoder configured to communicatively couple to an analog video transmission line for transmitting said encoded digital video data, wherein said output is configured to communicatively couple to a New Analog Video Interface cable.

11.   (Cancelled)

12.   (Original) The system as recited in Claim 11 wherein said output is configured to communicatively couple to a VGA adapter.

13.   (Original) The system as recited in Claim 10 wherein said digital to analog video encoder encodes said bits per color per pixel to one of eight distinguishable amplitude levels.

14.   (Original) The system as recited in Claim 10 wherein said digital to analog video encoder encodes three bits per said color per said pixel and wherein said three bits are encoded to an amplitude level between 0.0 volts and 0.7 volts.

15.   (Original) The system as recited in Claim 10 further comprising:

a multiplexer coupled to said digital to analog video encoder for separating said bits per color per pixel into even and odd bits.

16.   (Original) The system as recited in Claim 15 further comprising:
a symbol-rate clock coupled to said output configured such that said even and said odd bits can be transmitted in successive clock cycle of said symbol-rate clock.

17.   (Currently Amended) A method for encoding digital video data to an analog compatible format comprising:
accessing a plurality of bits per color per pixel of digital video data;
encoding said bits per color per pixel to one of a plurality of available amplitude levels;
separating said plurality of bits per color per pixel into even bits and odd bits; [[and]]
transmitting said even bits and said odd bits in successive clock cycles of a symbol rate clock; and
transmitting said even bits and said odd bits over a New Analog Video Interface (NAVI).

18.   (Original) The method as recited in Claim 17 further comprising:
restricting said available amplitude levels to eight levels.

19.   (Original) The method as recited in Claim 18 further comprising:
encoding a first set of bits of said bits per color per pixel to one of said eight available amplitude levels and transmitting said encoded first set of bits of said bits per color per pixel.

20.   (Original) The method as recited in Claim 19 further comprising:
encoding a second set of bits of said bits per color per pixel to one of said eight available amplitude levels and transmitting said encoded second set of bits of said bits per color per pixel.

21.   (Cancelled)

22.   (Original) The method as recited in Claim 17 further comprising:

decoding said amplitude level to a brightness level compatible with a fixed-format video display.

23.   (Original) The method as recited in Claim 17 further comprising:
encoding said bits of said bits per color per pixel of said digital video data to an amplitude level between 0.0 volts and 0.7 volts.

24.   (Currently Amended) An encoder for encoding digital video data to be transmitted over an analog interface comprising:
an input for receiving digital video data comprising a number of bits per color per pixel;
an encoder module coupled to said input for encoding said digital video data such that analog compatibility standards are preserved and said bits per color per pixel are encoded to an amplitude level; and
an output coupled to said encoder module configured to communicatively couple to an analog video transmission line for transmitting said encoded digital video data, wherein said output is configured to communicatively couple to a New Analog Video Interface cable.

25.   (Cancelled) The encoder as recited in Claim 24 wherein said output is configured to communicatively couple to a New Analog Video Interface cable.

26.   (Currently Amended) The encoder as recited in Claim [[25]] 24 wherein said output is configured to communicatively couple to a VGA adapter.

27.   (Original) The encoder as recited in Claim 24 wherein said encoder module encodes said bits per color per pixel to one of eight distinguishable amplitude levels.

28.   (Original) The encoder as recited in Claim 24 wherein said encoder module encodes three bits per said color per said pixel and wherein said three bits are encoded to an amplitude level between 0.0 volts and 0.7 volts.

29.   (Original) The encoder as recited in Claim 24 further comprising:
a multiplexer coupled to said digital to encoder module for separating said bits per color per pixel into even and odd bits.